



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. Of: ITO
Serial No.: 09/735,005
Filed: December 12, 2000
For: SEMICONDUCTOR DEVICE HAVING DUMMY GATES...
Group: 2811
Examiner: GEBREMARIAM, Samuel A. DOCKET: NEC 444

MAIL STOP - ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

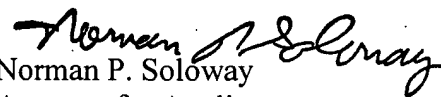
REQUEST FOR CORRECTED NOTICE OF ALLOWABILITY

Dear Sir:

The Notice of Allowability mailed December 16, 2005 contains an error at cipher 3. The claim for foreign priority is not acknowledged in the Notice. A certified copy of Japanese Patent Application No. 11-352847 was provided at the time of filing of the subject Application, as indicated on the Patent Application postcard, and the U.S. Filing Receipt mailed February 20, 2001. A copy of the stamped postcard is enclosed.

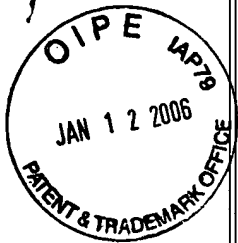
It is respectfully requested that a Corrected Notice of Allowability be issued.

Respectfully submitted,


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CERTIFICATE OF MAILING

I certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: MAIL STOP - ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 6, 2006 at Tucson, Arizona.

By: Kim Good

NPS:kg

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PATENT APPLICATION

Jc893 U.S. PTO
09/735005
12/12/00

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For: SEMICONDUCTOR DEVICE HAVING DUMMY GATES...
Docket: NEC 444

Received:

1. Transmittal letter (1 pg)
2. Form PTO-2038
3. Specification (10 pgs); claims (3 pgs); Abstract (1 pg)
with Declaration affixed thereto
4. 16 Sheets of Drawings
5. Assignment/Cover Sheet (3 pgs)
6. Prior Art Stmt (2 pgs); PTO-1449 (1 pg) with 1 reference
affixed thereto
7. Certified copy of Japanese Appln 11-352847

DEC 26 2000

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